

103 104 105 106 107 108 109 110 111 112

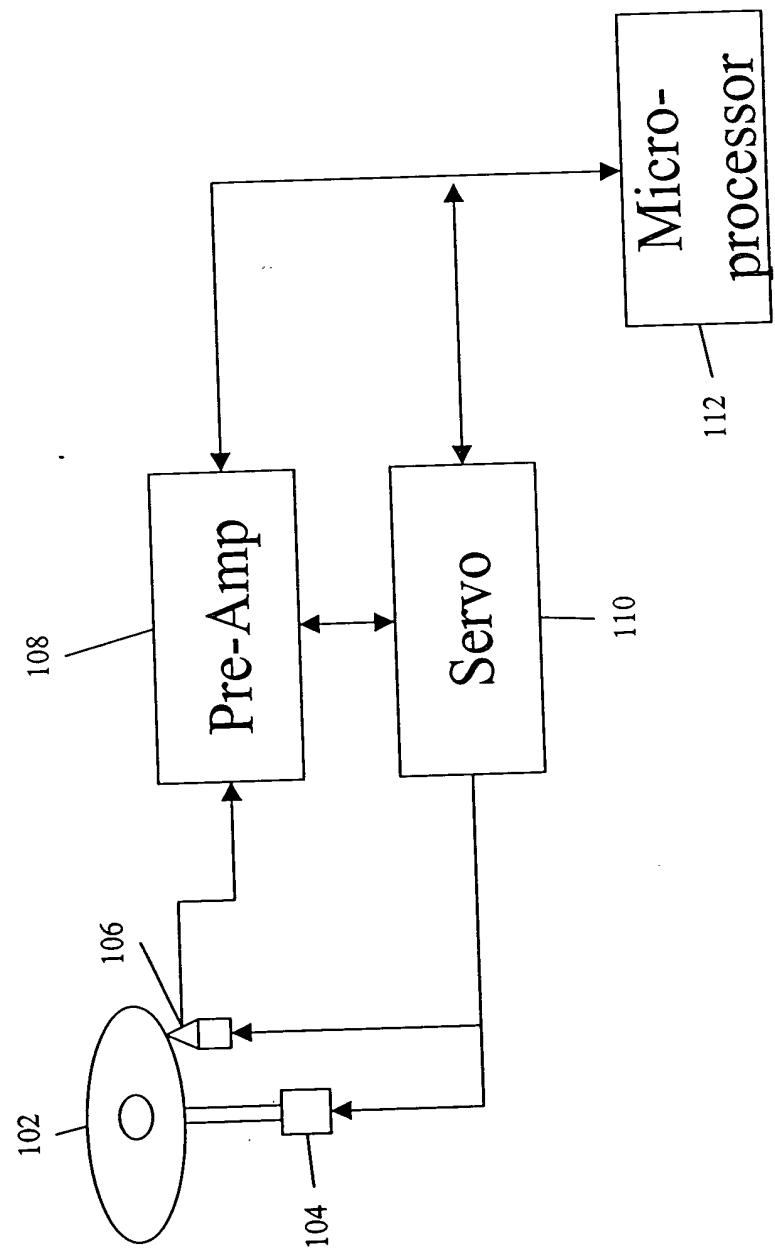


Figure 1  
(Prior Art)

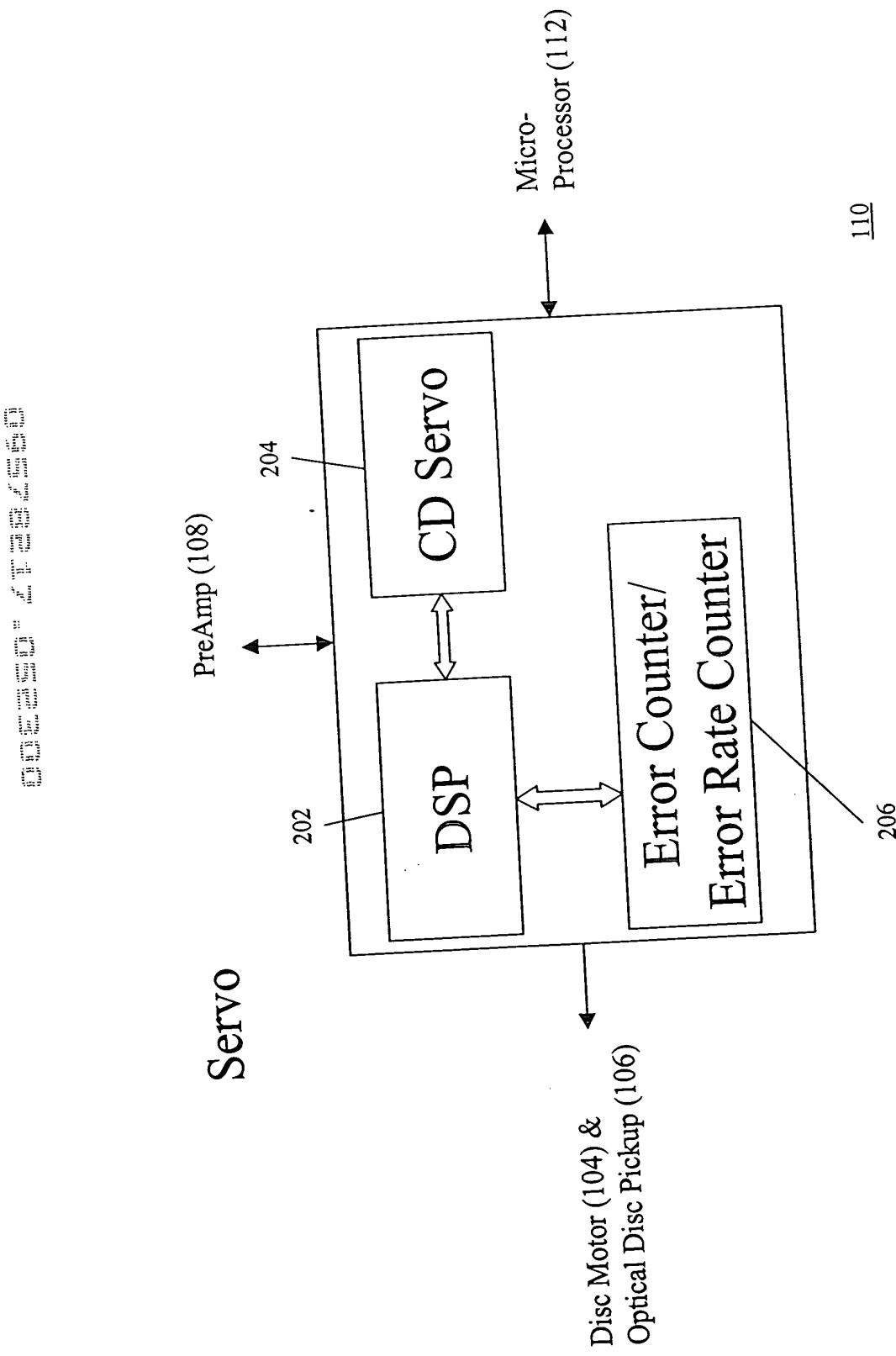


Figure 2

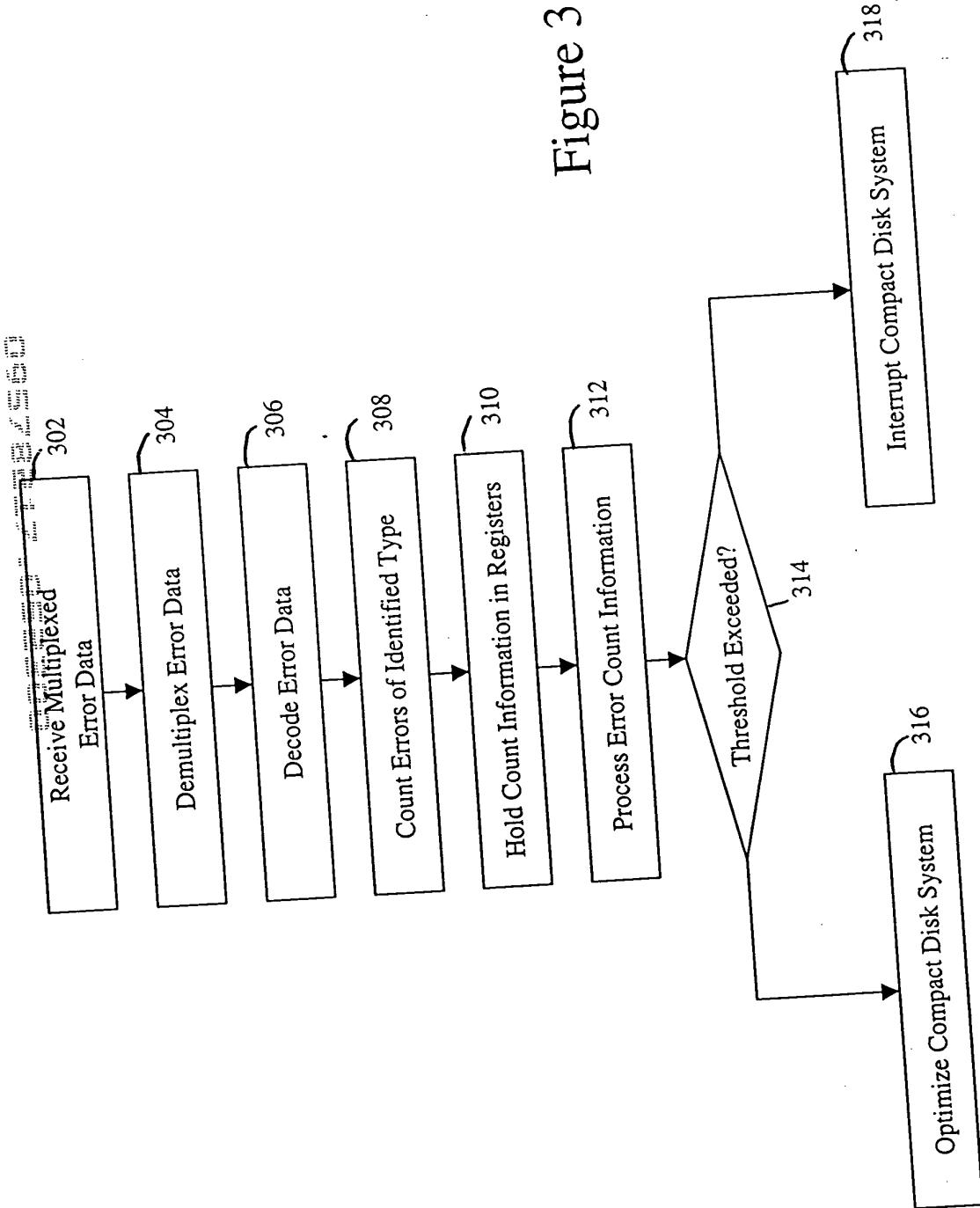


Figure 3

Time Demultiplex of  
Error Flag Signal

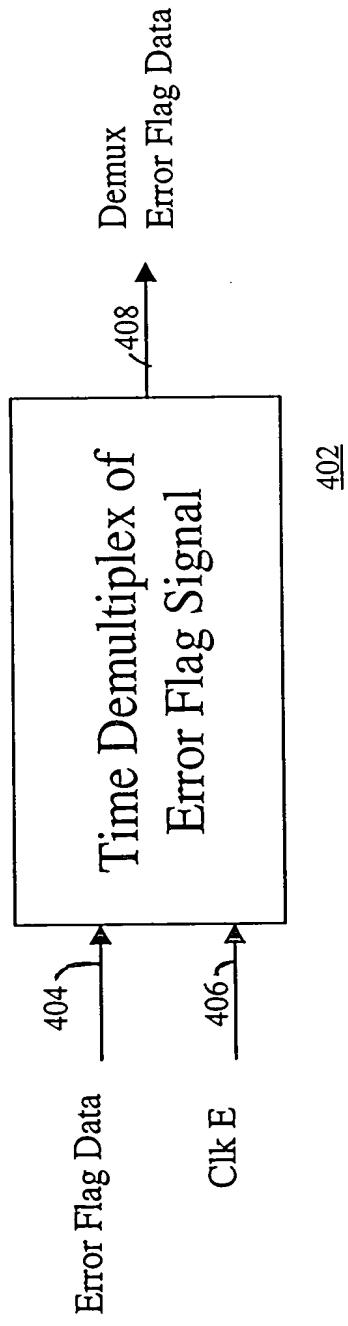
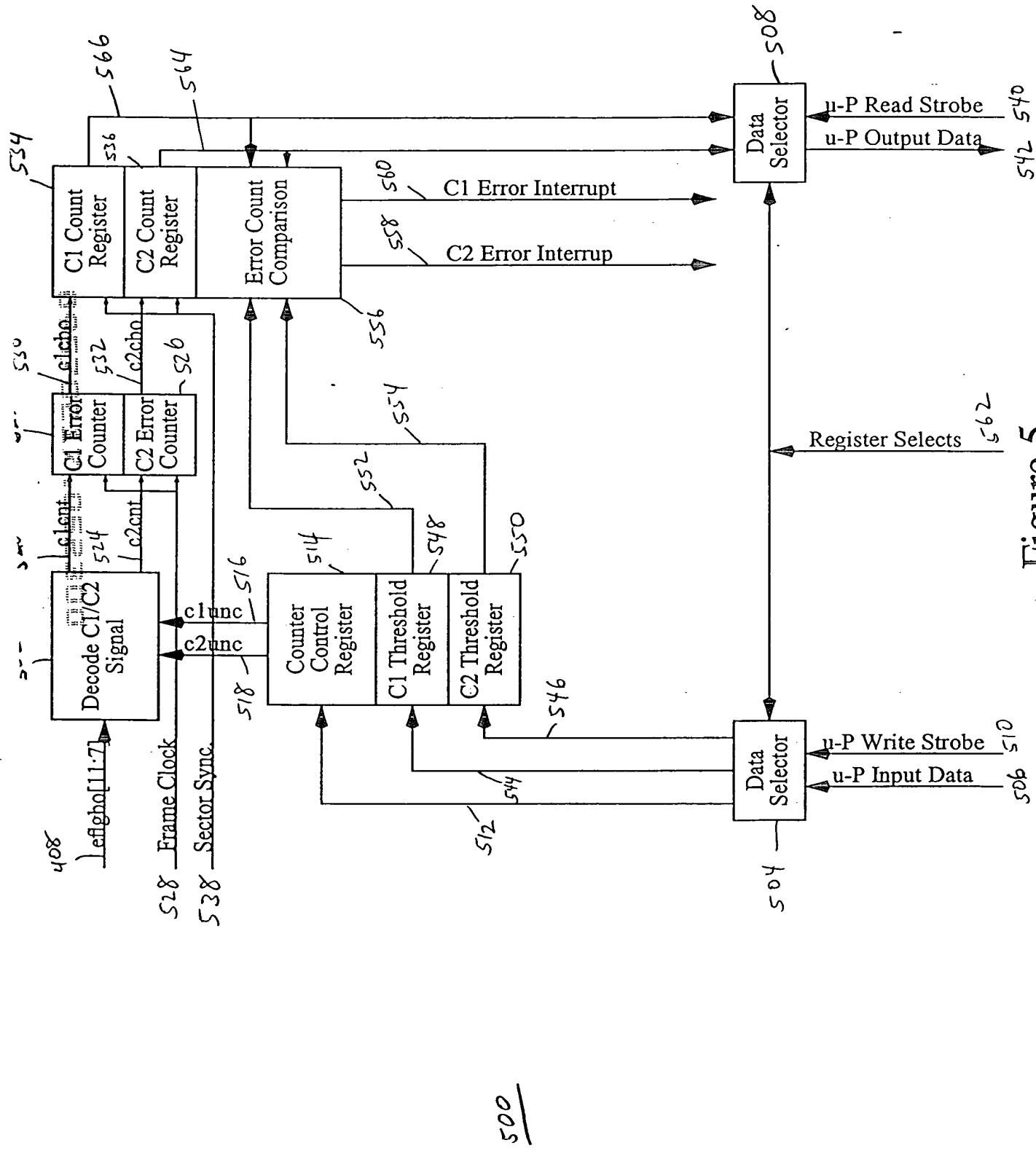


Figure 4

Figure 5



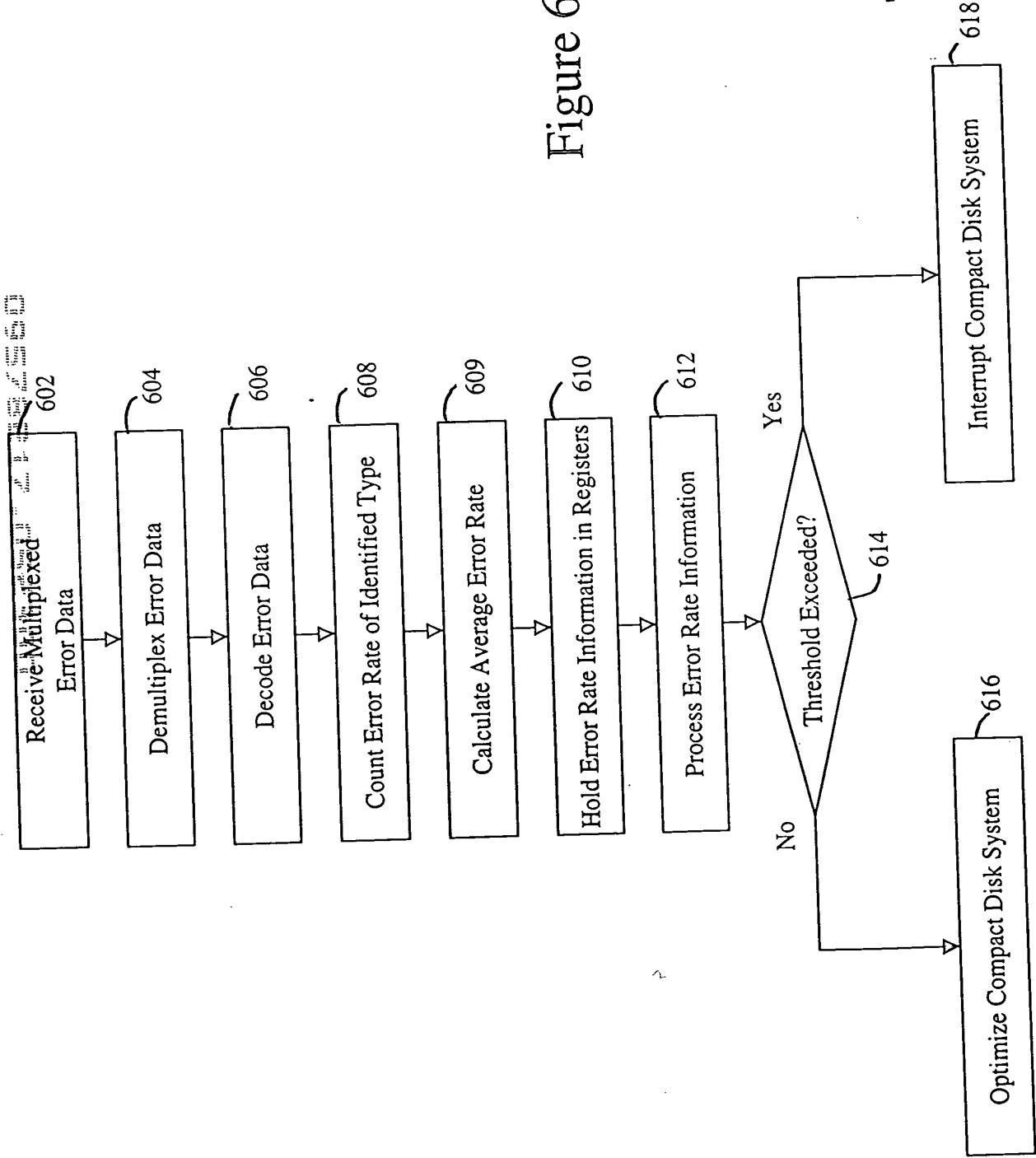


Figure 6

Figure 7

